IFP Parameters by Groups

R.H.F. **21/12/20 8:11:17 PM**

Defaults Group

Param	Name	Value Range	Default	Type	Comment	Notes
<26 >	I/P AGC Control	{Auto, Manual}	Auto	Primary		
<27 >	Manual Gain	[0..1023]	512	Dependent	Enabled by <26>=M	
<28 >	I/P Offset Control	{Auto, Manual}	Auto	Primary		
<29 >	Manual Offset	[0..1023]	512	Dependent	Enabled by <28>=M	
<23 >	BUL Thresh Ctrl	{Auto, Manual}	Auto	Primary		
<24 >	BU Man Thresh	[0..255]	140	Dependent	Enabled by <23>=M	
<25 >	BL Man Thresh	[0..255]	140	Dependent	Enabled by <23>=M	
<63 >	FUL Thresh Ctrl	{Auto, Manual}	Auto	Primary		
<64 >	FU Man Thresh	[0..255]	140	Dependent	Enabled by <63>=M	
<65 >	FL Man Thresh	[0..255]	140	Dependent	Enabled by <63>=M	
<9 >	BS Adders Ctrl	{Auto, Manual}	Auto	TestOnly		
<10 >	BS Adders Mode	{Bypass, Add}	Bypass	Dependent	Enabled by <9>=M	
<19 >	BU Mux Control	{Auto, Manual}	Auto	TestOnly		
<20 >	BU Data Select	{BU, BL}	BU	Dependent	Enabled by <19>=M	
<21 >	BL Mux Control	{Auto, Manual}	Auto	TestOnly		
<22 >	BL Data Select	{BU, BL}	BU	Dependent	Enabled by <21>=M	
<14 >	FT I/P Enable	{BUL_Data, Clamp1}	BUL_Data	TestOnly		* 5
<45 >	LO Phase Sync	{Off, On}	On	Primary		
<13 >	LO Sync Source	{SYNC, 1PPS_AUX}	SYNC	Primary		
<46 >	Add Frequ Offset	{Off, On}	Off	Primary		
<42 >	LO Frequ Offset	[0..BUL]	0.0	Primary		
<43 >	LO Phase Offset	[0..360]	0.0	Primary		
<48 >	LO Test Mode	{CW, Phase}	CW	TestOnly		
<51 >	FT Adders Ctrl	{Auto, Manual}	Auto	TestOnly		
<52 >	FT Adders Mode	{Bypass, Add}	Bypass	Dependent	Enabled by <51>=M	
<59 >	FU Mux Control	{Auto, Manual}	Auto	TestOnly		
<60 >	FU Data Select	{FU, FL}	FU	Dependent	Enabled by <59>=M	
<61 >	FL Mux Control	{Auto, Manual}	Auto	TestOnly		
<62 >	FL Data Select	{FU, FL}	FU	Dependent	Enabled by <61>=M	
<68 >	DIG Port Enable	{Tristate, Enabled}	Enabled	Primary		
<77 >	AT Corr Clk Delay	[0..3]	0	Primary		

Inputs Group

Param	Name	Value Range	Default	Type	Comment	Notes
<1 >	Image Filter	{Out, In}	Out	Primary		
<2 >	I/P Data Format	{8bit, 4bit}	8bit	Primary		
<4 >	I/P Spectrum	{Natural, Flipped}	Natural	Primary		
<30 >	64M O/P Spectrum	{Natural,Flipped}	Natural	Primary		

Outputs Group

Param	Name	Value Range	Default	Type	Comment	Notes
<7>	BS Q Filter*	[1..64]*	7	Primary		* 1,2
<11>	BU O/P Spectrum*	{Natural, Flipped}	Natural	Primary		* 3
<12>	BL O/P Spectrum*	{Natural, Flipped}*	Natural	Primary		* 4
<41>	LO Frequency	[0..BUL]	8.0E6	Primary		
<49>	FT Q Filter*	[1..64]*	9	Primary		* 5,6,7
<53>	FU O/P Spectrum*	{Natural, Flipped}	Natural	Primary		* 7,8
<54>	FL O/P Spectrum*	{Natural, Flipped}	Natural	Primary		* 7,9
<31>	Magnitude Stats	{4-level, 3-level}	4-level	Primary		
<70>	S2 Data Source	{BxFx, 32M, 64M, 64Mm}*	BxFx	Primary		* 10
<71>	S2 Lo Chans Data	{BU, BL, FU, FL}*	BU	Dependent	Enabled by <70>=BxFx	* 3,8,11,12
<72>	S2 Hi Chans Data	{BU, BL, FU, FL}	FU	Dependent	Enabled by <70>=BxFx	* 3,8,11,12
<73>	S2 Lo Chans Fmat	{AT, VLBA}	AT	Dependent	En by <70>=BxFx, 32M	
<74>	S2 Hi Chans Fmat	{AT, VLBA}*	AT	Dependent	En by <70>=BxFx, 32M	* 13
<47>	LO Phase Correct	{Off, On}*	On	Primary	On requires <45>=On	* 16
<3>	I/P Window	{Outer, Inner}	Inner	Secondary	Set by <7>	
<5>	Pos Hilb Filter	[1..64]	31	Secondary	Set by <7>	
<6>	Neg Hilb Filter	[1..64]	31	Secondary	Identical to <5>	
<8>	BS I Filter	[1..64]	7	Secondary	Set by <7>	
<15>	FT I/P Data Ctrl	{Auto, Manual}	Auto	Secondary	Set by <7>	
<40>	Clock Decimation	[1,2,4,8,16]	1	Virtual	Set by <7>	
<44>	LO Timer Interval	[0..1]	0.0	Secondary	Set by <40> & <41>	
<50>	FT I Filter	[1..64]	9	Secondary	Set by <49>	
<17>	BS Monitor Ctrl	{Auto, Manual}	Auto	Secondary	Set by <7>	
<57>	FT Monitor Ctrl	{Auto, Manual}	Auto	Secondary	Set by <49>	
<55>	DIG Port Ctrl	{Auto, Manual}	Auto	Secondary	Set by <49>	

Monitors Group

Param	Name	Value Range	Default	Type	Comment	Notes
<18>	BS Mon BandSel	{BU, BL}	BU	Dependent	Enabled by <17>=M	
<16>	FT I/P BandSel	{BU, BL}	BU	Dependent	Enabled by <15>=M	
<58>	FT Mon BandSel	{FU, FL}	FU	Dependent	Enabled by <57>=M	
<56>	DIG Port BandSel	{FU, FL}	FU	Dependent	Enabled by <55>=M	
<75>	CORR OUT Fmat	{AT, Multibeam}*	AT	Primary		* 14
<76>	Corr Data Source	{BU,BL,FU,FL, 32M, 64M}	BU	Dependent	Enabled by <70>=BxFx	*
<79>	MB Corr Source 2	{BU, BL, FU, FL}	BL	Primary		

Serial Group

Param	Name	Value Range	Default	Type	Comment	Notes
<101>	Com Port	[1..4]	1	Primary		

<102>	Data Set Address	[0..31]	0	Primary
<105>	Baud Rate	[4800,38400]	38400	Primary
<103>	IFP 1 Address	[0,1]	0	TestOnly
<104>	IFP 2 Address	[0,1]	1	TestOnly
<106>	Parity	{Odd}	Odd	Fixed
<107>	Data Bits	[8]	8	Fixed
<108>	Stop Bits	[1]	1	Fixed