21/12/20 8:09:31 PM

IFP Parameter List

R.H.F.

Param	Name	Value Range	Default	Group	Type	Comment
<1>	Image Filter	{Out, In}	Out	Inputs	Primary	
<2>	I/P Data Format	{8bit, 4bit}	8bit	Inputs	Primary	
<3>	I/P Window	{Outer, Inner}	Inner	Outputs	Secondary	Set by <7>
<4>	I/P Spectrum	{Natural, Flipped}	Natural	Inputs	Primary	
<5>	Pos Hilb Filter	[1..64]	31	Outputs	Secondary	Set by <7>
<6>	Neg Hilb Filter	[1..64]	31	Outputs	Secondary	Identical to <5>
<7>	BS Q Filter*	[1..64]*	7	Outputs	Primary	
<8>	BS I Filter	[1..64]	7	Outputs	Secondary	Set by <7>
<9>	BS Adders Ctrl	{Auto, Manual}	Auto	Defaults	TestOnly	
<10>	BS Adders Mode	{Bypass, Add}	Bypass	Defaults	Dependent	Enabled by <9>=M
<11>	BU O/P Spectrum*	{Natural, Flipped}	Natural	Outputs	Primary	
<12>	BL O/P Spectrum*	{Natural, Flipped}*	Natural	Outputs	Primary	
<13>	LO Sync Source	{SYNC, 1PPS_AUX}	SYNC	Defaults	Primary	
<14>	FT I/P Enable	{BUL_Data, Clamp1}	BUL_Data	Defaults	TestOnly	
<15>	FT I/P Data Ctrl	{Auto, Manual}	Auto	Outputs	Secondary	Set by <7>
<16>	FT I/P BandSel	{BU, BL}	BU	Monitors	Dependent	Enabled by <15>=M
<17>	BS Monitor Ctrl	{Auto, Manual}	Auto	Outputs	Secondary	Set by <7>
<18>	BS Mon BandSel	{BU, BL}	BU	Monitors	Dependent	Enabled by <17>=M
<19>	BU Mux Control	{Auto, Manual}	Auto	Defaults	TestOnly	
<20>	BU Data Select	{BU, BL}	BU	Defaults	Dependent	Enabled by <19>=M
<21>	BL Mux Control	{Auto, Manual}	Auto	Defaults	TestOnly	
<22>	BL Data Select	{BU, BL}	BU	Defaults	Dependent	Enabled by <21>=M
<23>	BUL Thresh Ctrl	{Auto, Manual}	Auto	Defaults	Primary	
<24>	BU Man Thresh	[0..255]	140	Defaults	Dependent	Enabled by <23>=M
<25>	BL Man Thresh	[0..255]	140	Defaults	Dependent	Enabled by <23>=M
<26>	I/P AGC Control	{Auto, Manual}	Auto	Defaults	Primary	
<27>	Manual Gain	[0..1023]	512	Defaults	Dependent	Enabled by <26>=M
<28>	I/P Offset Control	{Auto, Manual}	Auto	Defaults	Primary	
<29>	Manual Offset	[0..1023]	512	Defaults	Dependent	Enabled by <28>=M
<30>	64M O/P Spectrum	{Natural,Flipped}	Natural	Inputs	Primary	
<31>	Magnitude Stats	{4-level, 3-level}	4-level	Outputs	Primary	
<40>	Clock Decimation	[1,2,4,8,16]	1	Outputs	Virtual	Set by <7>
<41>	LO Frequency	[0..BUL]	8.0E6	Outputs	Primary	
<42>	LO Frequ Offset	[0..BUL]	0.0	Defaults	Primary	
<43>	LO Phase Offset	[0..360]	0.0	Defaults	Primary	
<44>	LO Timer Interval	[0..1]	0.0	Outputs	Secondary	Set by <40> & <41>
<45>	LO Phase Sync	{Off, On}	On	Defaults	Primary	
<46>	Add Frequ Offset	{Off, On}	Off	Defaults	Primary	
<47>	LO Phase Correct	{Off, On}*	On	Outputs	Primary	On requires <45>=On

Param	Name	Value Range	Default	Group	Type	Comment
<48>	LO Test Mode	{CW, Phase}	CW	Defaults	TestOnly	
<49>	FT Q Filter*	[1..64]*	9	Outputs	Primary	
<50>	FT I Filter	[1..64]	9	Outputs	Secondary	Set by <49>
<51>	FT Adders Ctrl	{Auto, Manual}	Auto	Defaults	TestOnly	
<52>	FT Adders Mode	{Bypass, Add}	Bypass	Defaults	Dependent	Enabled by <51>=M
<53>	FU O/P Spectrum*	{Natural, Flipped}	Natural	Outputs	Primary	
<54>	FL O/P Spectrum*	{Natural, Flipped}	Natural	Outputs	Primary	
<55>	DIG Port Ctrl	{Auto, Manual}	Auto	Outputs	Secondary	Set by <49>
<56>	DIG Port BandSel	{FU, FL}	FU	Monitors	Dependent	Enabled by <55>=M
<57>	FT Monitor Ctrl	{Auto, Manual}	Auto	Outputs	Secondary	Set by <49>
<58>	FT Mon BandSel	{FU, FL}	FU	Monitors	Dependent	Enabled by <57>=M
<59>	FU Mux Control	{Auto, Manual}	Auto	Defaults	TestOnly	
<60>	FU Data Select	{FU, FL}	FU	Defaults	Dependent	Enabled by <59>=M
<61>	FL Mux Control	{Auto, Manual}	Auto	Defaults	TestOnly	
<62>	FL Data Select	{FU, FL}	FU	Defaults	Dependent	Enabled by <61>=M
<63>	FUL Thresh Ctrl	{Auto, Manual}	Auto	Defaults	Primary	
<64>	FU Man Thresh	[0..255]	140	Defaults	Dependent	Enabled by <63>=M
<65>	FL Man Thresh	[0..255]	140	Defaults	Dependent	Enabled by <63>=M
<68>	DIG Port Enable	{Tristate, Enabled}	Enabled	Defaults	Primary	
<70>	S2 Data Source	{BxFx, 32M, 64M, 64Mm}*	BxFx	Outputs	Primary	
<71>	S2 Lo Chans Data	{BU, BL, FU, FL}*	BU	Outputs	Dependent	Enabled by <70>=BxFx
<72>	S2 Hi Chans Data	{BU, BL, FU, FL}	FU	Outputs	Dependent	Enabled by <70>=BxFx
<73>	S2 Lo Chans Fmat	{AT, VLBA}	AT	Outputs	Dependent	En by <70>=BxFx, 32M
<74>	S2 Hi Chans Fmat	{AT, VLBA}*	AT	Outputs	Dependent	En by <70>=BxFx, 32M
<75>	CORR OUT Fmat	{AT, Multibeam}*	AT	Monitors	Primary	
<76>	Corr Data Source	{BU,BL,FU,FL, 32M, 64M}	BU	Monitors	Dependent	Enabled by <70>=BxFx
<77>	AT Corr Clk Delay	[0..3]	0	Defaults	Primary	
<79>	MB Corr Source 2	{BU, BL, FU, FL}	BL	Monitors	Primary	
<101>	Com Port	[1..4]	1	Serial	Primary	
<102>	Data Set Address	[0..31]	0	Serial	Primary	
<103>	IFP 1 Address	[0,1]	0	Serial	TestOnly	
<104>	IFP 2 Address	[0,1]	1	Serial	TestOnly	
<105>	Baud Rate	[4800,38400]	38400	Serial	Primary	
<106>	Parity	{Odd}	Odd	Serial	Fixed	
<107>	Data Bits	[8]	8	Serial	Fixed	
<108>	Stop Bits	[1]	1	Serial	Fixed	