

vex2 support in vex2difx

Implemented features

- Recognition that a supplied vex file is vex2 rather than vex1.5

Problematic features

- *None yet*

Wish list

If you have a particular vex2 feature that is not yet implemented, note it here.

- Proper support for VDIF through use of the DATASTREAMS block
- Support for higher-order clock terms in the CLOCK table
- Capture of project code and segment code separately
- Support of the BITSTREAMS block for VSI-H data formats
- Support updates to IF block
- Support updates to FREQ block, including complex samples

References

- [vex2 definition](#)
- [vex2 branch of vex2difx](#)

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<https://www.atnf.csiro.au/vlbi/dokuwiki/> - **ATNF VLBI Wiki**

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