

Ref and W104 Tied ants CA01..CA05

CABB board died- Possible marginal data ~03:00-03:3. Off sourcee ~03:30-04:00. Should be OK from 04:00 UTC

RECALIBRATED ARRAY BETWEEN 03:30 and 04:00 UTC - DELAYS WILL HAVE CHANGED

Correlation note added 21 Jan 2013:

Delays changed by -5 us at 04:00 UT. Changed again by +5 us at 09:45 (presumably due to another CABB reboot/dcal required?)

Clock model used:

```
$CLOCK;  
def AT;  
* Valid from clock_early clock_early_epoch rate
```

```
'clock_early = 2012y319d00h00m00s : 39.9087145896 usec :  
2012y319d01h14m00s : 0.0;'  
'clock_early = 2012y319d04h00m00s : 34.7787145896 usec :  
2012y319d01h14m00s : 0.0;'  
'clock_early = 2012y319d09h45m00s : 40.0287145896 usec :  
2012y319d01h14m00s : 0.0;'
```

```
endif;
```

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